

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANTS
PTO-1449**

Attorney Docket No. 2885/96	Serial No. 10/551,891
Applicant(s) Martin VORBACH	
Filing Date August 28, 2006	Group Art Unit 2183

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT/ PUBLICATION NUMBER	PATENT/PUBLICATION DATE	NAME	CLASS	SUBCLASS	FILING DATE
	5,525,971	06-11-1996	Flynn			

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS

EXAMINER'S INITIALS	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
	Advanced RISC Machines, "Introduction to AMBA," Section 1, pp. 1-1 to 1-7 (October 1996)
	ARM, "The Architecture for the Digital World," http://www.arm.com/products , 3 pages (March 18, 2009)
	ARM, "The Architecture for the Digital World; Milestones," http://www.arm.com/aboutarm/milestones.html , 5 pages (March 18, 2009)
	Del Corso, et al., "Microcomputer Buses and Links," Academic Press Inc. Ltd., pp. 138-143, 277-285 (1986)
	"IEEE Standard Test Access Port and Boundary-Scan Architecture," IEEE Std. 1149.1-1990, pp. 1-127 (1993)
	PCI Local Bus Specification, Production Version, Revision 2.1, Portland, OR, pp. 1-281 (June 1, 1995)
	"The Programmable Logic Data Book," XILINX, Inc., Section 2, pp. 1-240, Section 8, pp. 1, 23-25, 29, 45-52, 169-172 (1994)

EXAMINER /Keith Vicary/	DATE CONSIDERED 07/01/2009
EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. Page 7/KV/